

## **AMENDMENTS TO THE CLAIMS**

Please cancel Claims 2-7, 10-16 and 20-23 as follows.

### **LISTING OF CLAIMS**

1. (original) A gain control device comprising:
  - a variable gain amplifier for amplifying an input signal with a gain corresponding to a control voltage applied thereto;
  - a power detector for outputting an output voltage corresponding to an input power or an output power of said variable gain amplifier;
  - a control circuit for outputting the control voltage based on an output voltage of said power detector for controlling the output power of said variable gain amplifier with respect to a target value;
  - a packet detection circuit for detecting a packet signal by means of the output voltage from said power detector and outputting a packet detection signal;
  - a timing circuit for outputting a timing signal after an elapse of a predetermined time after a start of outputting the packet detection signal from said packet detection circuit;
  - a sample-hold circuit for sampling-and-holding the control voltage from said control circuit in response to the timing signal from said timing circuit; and
  - a switch circuit for providing the control voltage from said control circuit for said variable gain amplifier until the timing signal is output from said timing circuit, and

providing the control voltage held by said sample-hold circuit for said variable gain amplifier in response to outputting of the timing signal.

2-7. (cancelled)

8. (original) A gain control device as set forth in claim 1, wherein the packet signal includes a preamble, and the predetermined time is shorter than the receiving time of the preamble but longer than the time the output power of said variable gain amplifier converges to the target value.

9. (original) Again control device as set forth in claim 8, wherein the preamble includes a preamble portion for gain control, and the predetermined time is shorter than the receiving time of the preamble portion for gain control.

10-16. (cancelled)

17. (original) A gain control method for a receiver in a packet communication system in which each packet signal includes a preamble at a head and data subsequent to the preamble, the method comprising the steps of:

- amplifying a received packet signal with a gain;
- detecting an output power of the amplified signal;
- generating a control voltage variable with the detected output power; and

controlling the gain based on the control voltage so that the output power approaches a target value.

18. (original) A gain control method for a receiver as set forth in claim 17, further comprising the steps of:

detecting a start of the preamble of the packet signal by means of the detected output power;

measuring an elapsed time after a detection of the start of the preamble of the packet signal; and

switching from high-speed gain control to low-speed gain control when the measured elapsed time exceeds a predetermined time.

19. (original) A gain control method for a receiver as set forth in claim 18, wherein the switching step comprises the steps of:

sampling-and-holding the control voltage when the measured elapsed time exceeds the predetermined time; and

fixing the gain in the low-speed gain control based on the held control voltage for providing stable output power during a reception of the data of the packet signal.

20-23. (cancelled)